

PACKAGE OUTLINE DRAWING

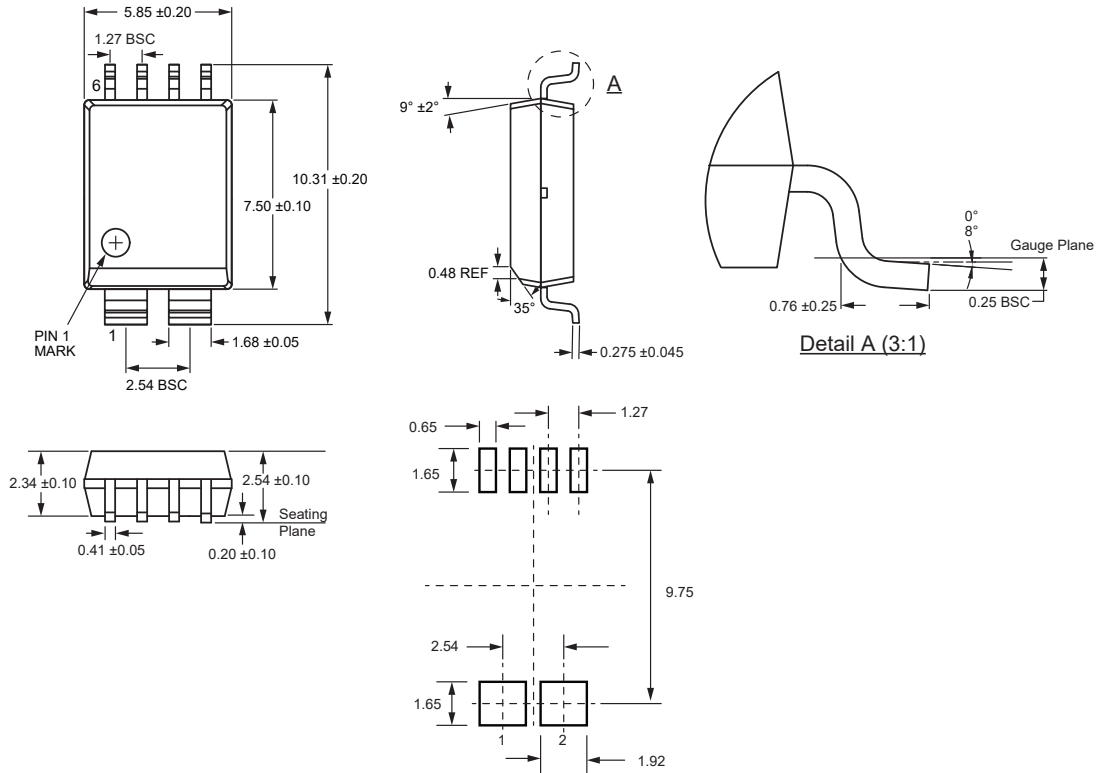
For Reference Only – Not for Tooling Use

(Reference Allegro DWG-0000388, Rev. 2)

NOT TO SCALE

Dimensions in millimeters

Dimensions exclusive of mold flash, gate burrs, and dambar protrusions
 Exact case and lead configuration at supplier discretion within limits shown



PCB Layout Reference View

All pads a minimum of 0.20 mm from all adjacent pads;
 adjust as necessary to meet application process
 requirements and PCB layout tolerances

6-Pin fused-lead SOIC (suffix MZ)